NANO

2008 Vol. 8, No. 7 1912-1915

Crystallographic Etching of Few-Layer Graphene

Sujit S. Datta,† Douglas R. Strachan,*,†,‡ Samuel M. Khamis,† and A. T. Charlie Johnson*,†,‡

Department of Physics and Astronomy, and Department of Materials Science and Engineering, University of Pennsylvania, Philadelphia, Pennsylvania 19104

Received February 27, 2008; Revised Manuscript Received June 9, 2008

ABSTRACT

We demonstrate a method by which few-layer graphene samples can be etched along crystallographic axes by thermally activated metallic nanoparticles. The technique results in long (>1 \(\mu\min\) crystallographic edges etched through to the insulating substrate, making the process potentially useful for atomically precise graphene device fabrication. This advance could enable atomically precise construction of integrated circuits from single graphene sheets with a wide range of technological applications.

Due to its remarkable electronic properties, few layer graphene is emerging as a promising new material for use in a vast array of postsilicon nanoelectronic devices incorporating quantum size effects.^{1,2} Of particular interest would be the construction of atomically precise graphene nanoribbons, in which charge carriers are confined in the lateral dimension whereby the electronic properties are controlled by the width and specific crystallographic orientation of the ribbon.³⁻¹⁴ Such structures hold enormous promise as nanoscale devices similar to those recently developed using carbon nanotubes^{2,11,15} with the added advantage that graphene's two-dimensionality lends itself to existing device architectures based on planar geometries.

However, these structures have so far been impossible to achieve because of the rough noncrystalline edges of the graphene that result from current state-of-the-art nanolithography techniques.^{2,16,17} These rough edges are thought to be the crucial limiting factor to attaining useful performance and on/off current ratios from nanoscale graphene devices. 13,18,19

As a step toward band gap engineering of this material, we have developed a means by which few-layer graphene (FLG) samples can be etched along crystallographic axes by thermally activated metallic nanoparticles.²⁰ The technique results in long (>1 μ m) trenches commensurate with the crystal lattice that are etched through to the supporting insulating substrate, making the process potentially useful for atomically precise graphene device fabrication, as well as indicating a possible method by which entire circuits could be carved out from single graphene sheets.¹

Our initial samples (before etching) consisted of pristine few-layer graphene sheets transferred onto highly doped Si substrates with 300 nm thermally grown SiO₂ by mechanical exfoliation under ambient conditions, similar to the technique described in ref 21. Flakes of few-layer graphene are identified using optical microscopy. Height imaging of our samples is done using a Veeco Dimension 3100 atomic force microscope (AFM) operating in intermittent contact mode, with Si₃N₄-coated tips (NSC15, Mikromasch) of curvature radius ≤ 20 nm. Samples are also characterized using a JEOL JSM6400 scanning electron microscope (with a LaB₆ filament).

The FLG on SiO₂/Si substrates are uniformly spin-coated with ~ 15 mL solution of 50 mg/L Fe(NO₃)₃•9H₂O in isopropyl alcohol (HPLC grade). The samples are then transferred to a furnace and heated in hydrogen and argon gas coflow (320 sccm/600 sccm, respectively) at 900 °C for 45 min. At these temperatures, the Fe accumulates to form small ~15 nm diameter nanoparticles which can diffuse along the surface of the SiO₂ and graphene.²² As the nanoparticles diffuse over the surface of the SiO2 and graphene at these elevated temperatures, they etch away the FLG sheets.

Figure 1 shows AFM and SEM images of FLG samples after this etching procedure. Crucially, we observe long and straight (>1 μ m) nanotrenches in the FLG along which graphene has been removed. The majority of these trenches are etched down to the insulating substrate. This is evident through contrast variations in SEM images and through detailed AFM height analysis of the nanotrenches (Figure 1). We find through cross-section analysis (as in Figure 2) that the depths of the trenches correspond to the difference in height between the bare substrate and the FLG flake. This

^{*} Corresponding author. E-mail: drstrach@sas.upenn.edu (D.S.) and cjohnson@physics.upenn.edu (C.J.).

Department of Physics and Astronomy.

^{*} Department of Materials Science and Engineering.

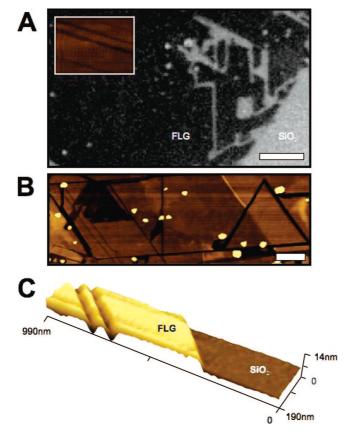


Figure 1. Single-particle crystallographic etching of few-layer graphene. (A) SEM image of nanotrenches etched in FLG (dark) supported on SiO₂/Si substrate (light); scale bar is 800 nm. Small circles are Fe nanoparticles. Inset image, width 315 nm, shows an AFM image (color scale 15 nm) of a graphene nanoribbon defined using parallel etched trenches, of measured width \sim 35 nm. (B) AFM image (color scale 15 nm) of etched FLG sample, showing straight nanotrenches etched down to the substrate defining a triangle of side length \sim 650 nm; scale bar is 250 nm. (C) 3D AFM surface plot of etched FLG sample, showing parallel straight nanotrenches.

indicates that the trench forming process results in tracks that are etched down to the insulating substrate. AFM imaging shows that the graphene surface has roughness on the order of 0.5 nm, which is the roughness of the substrate and is consistent with our and others' measurements of the intrinsic roughness of FLG on SiO₂:²³ this indicates that the graphene surface is largely unspoiled by the etching procedure. AFM analysis also reveals that the widths of the trenches are on the order of tens of nanometers and frequently less than 20 nm. In the AFM images, some narrow trenches do not appear to extend down to the underlying substrate which can be due to the fact that they are not fully resolved when they are narrower than the ~20 nm wide AFM tip.

Measurements of the lengths and orientations of etched FLG trenches reveal striking correlations with the graphene lattice. Figure 3 shows a histogram of total etched trench length versus angle for the FLG sample of Figure 2A. Typically, particle tracks travel predominantly along a single direction (defined as 0° in Figure 3) with other preferred directions at $\pm 60^{\circ}$ relative to this. Slightly less preferred directions at 30° intervals are also observed. It is currently not clear why a single etching direction is preferred. In

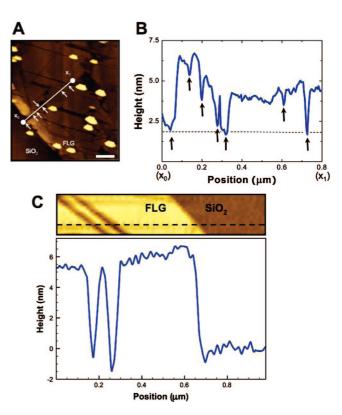


Figure 2. (A) AFM image of etched FLG sample; color scale is 14 nm. (B) Height profile along the line in (A), showing nanotrenches etched down to the underlying SiO₂/Si substrate: leftmost arrow indicates substrate, while other arrows indicate etched trenches. Because of the finite size of the AFM tip, measured nanotrench heights (for trenches of width < 20 nm) appear shorter. The height increase near the edge of the film is due to a fold in the FLG. (C) AFM image (color scale 14 nm) of etched FLG shown in Figure 1C with parallel nanotrenches etched by catalyst nanoparticles, with height profile along the dotted line showing nanotrenches etched down to the underlying SiO₂/Si substrate.

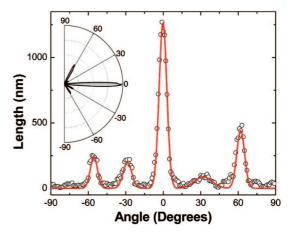


Figure 3. Histogram for AFM image of Figure 2A showing total etched nanotrench length versus trench angle, chosen so the main peak is at 0° (FLG edge is at $\sim 34^{\circ}$). Open circles show the data averaged over seven adjacent points; red curve is a five-peak Gaussian fit. Inset shows histogram data (open circles) as a polar plot.

addition, we note that no significant correlation was found between the orientation of the etched nanotrenches and the direction of gas flow. The existence of these track directions spaced at 30° intervals gives strong support to the notion

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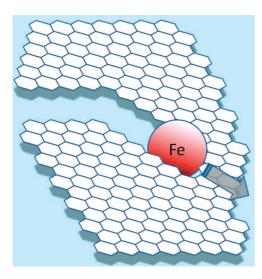


Figure 4. Schematic of crystallographic few-layer graphene (FLG) etching process. Thermally activated Fe nanoparticles react with active carbon atoms at graphene edges, moving and breaking FLG carbon—carbon bonds along specific crystallographic directions.

that the etched trenches are commensurate with the graphene honeycomb lattice; thus, our etching procedure could be potentially useful in constructing device edges that are oriented with the underlying crystal lattice of the graphene. This is in contrast to conventional nanolithography etching of FLG samples, which do not yield structures that are commensurate with the graphene lattice. While investigations of the atomic-scale details of our etched graphene edges are still underway, the observed long (on the order of a micrometer) trenches suggest that the edges have long-range order. In addition to other structures, we have used this single-particle etching technique to fabricate graphene nanoribbons of widths measured using AFM as small as \sim 15 nm and lengths on the order of micrometers (e.g., inset to Figure 1A).

FLG etching with metallic nanoparticles likely occurs by a hydrogenation mechanism similar to that studied by numerous researchers several decades ago for bulk carbon allotropes (e.g., ref 20). This reaction, catalyzed by the Fe nanoparticles, is given by

$$C(s) + 2H_2(g) \xrightarrow{Fe} CH_4(g)$$
 (1)

in which the graphene acts as the carbon source C(s). Further evidence that this reaction is the source of the crystal-lographic etching is found from experiments performed in hydrogen-free argon gas flow which do not result in nanotrench formation.²⁴

As the Fe particles diffuse on the SiO₂ surface, they react with active carbon atoms at the graphene edges. This initiates nanotrench formation, and the moving Fe nanoparticles remove carbon atoms in the graphene along tracks aligned with the underlying crystal lattice, forming the observed nanotrenches (Figure 4). Indeed, we observe that the vast majority of etched trenches commence from the edges of the FLG sheets, with an Fe nanoparticle at the end of each trench. Furthermore, isolated nanoparticles are sometimes observed on top of FLG flakes without any etch track,

indicating that carbon atoms at FLG edges (versus those on the FLG surface) are reactive and more likely to initiate nanotrench formation.

The fact that the etching occurs as the nanoparticles move over an amorphous SiO₂ surface indicates that the crystallographic orientation of the trenches are determined from the interfacial interactions of the nanoparticle with the edge of the FLG. This crystallographic etching could be due to the favorable adhesion and wetting of the Fe nanoparticles to the graphene edge along specific crystallographic directions.²⁵ Another potential mechanism for our crystallographic etching technique is crystallographic dependence of the Fe—graphene reactivity, for example, due to lowered activation energy of the reaction given in eq 1 along specific directions commensurate with the FLG lattice.^{20,26}

In conclusion, we have demonstrated a technique by which few-layer graphene can be etched along crystallographic directions down to the underlying substrate, a potentially useful technique for attaining further progress in the development of graphene nanoelectronic devices. This technique relies on the use of individual metal nanoparticles as catalysts to "carve out" long, straight nanotrenches with crystallographically oriented edges in FLG samples. The future development of techniques whereby the motion of the nanoparticles can be controlled will lead to possible exciting new graphene device architectures. This could enable the construction of integrated transistors, circuits, chemical sensors, and spin valves from single graphene sheets with many far-reaching potential applications.¹

Acknowledgment. This work was supported by the Nano/Bio Interface Center through the National Science Foundation NSEC DMR-0425780 and the JSTO DTRA, the Army Research Office Grant W911NF-06-1-0462, and the Intelligence Community Postdoctoral Fellowship Program.

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NL080583R

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